



3723
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9-16-03

PATENT

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September 9, 2003

Tamra Miller

Date

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Scott E. Moore Attorney Docket No.: 500199.02 (29243/US/1)
Serial No. : 09/927,295 Group Art Unit : 3723
Filed : August 10, 2001 Examiner : Willie Wendell Berry, Jr.
Title : METHOD AND APPARATUS FOR PLANARIZING A MICROELECTRONIC
 SUBSTRATE WITH A TILTED PLANARIZING SURFACE

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

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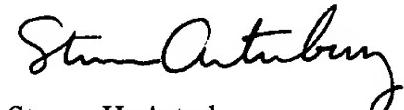
TECHNOLOGY CENTER R3700

In accordance with 37 C.F.R. §§ 1.56 and 1.97 through 1.98, applicant wishes to make known to the Patent and Trademark Office the references set forth on the attached form PTO-1449 (copies of the cited references, as required under 37 C.F.R. § 1.98, are enclosed). Although the aforesaid references are made known to the Patent and Trademark Office in compliance with applicant's duty to disclose all information he is aware of which is believed relevant to the examination of the above-identified application, applicant believes that his invention is patentable.

Please acknowledge receipt of this Information Disclosure Statement and kindly make the cited references of record in the above-identified application.

Respectfully submitted,

DORSEY & WHITNEY LLP



Steven H. Arterberry
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SHA:tlm

Enclosures:

- Postcard
- Form PTO-1449
- Cited References (17)

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